

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventors: James Stasiak et al. **Examiner:** Howard Weiss
Application No: 10/668,558 **Group Art Unit:** 2814
Filing Date: 9/22/2003 **Confirmation No:** 4709
Title: SEMICONDUCTOR DEVICES HAVING A NANOMETER SCALE
JUNCTION

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.113

Dear Sir:

In response to the Office Communication mailed April 24, 2006 please
reconsider the above-identified application in accordance with 37 C.F.R. § 1.113 as
follows:

Introductory comments begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.